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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE 7005 SEP 16 10

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Patentee:

Kuo-Chi Tu, et al.

Docket No .:

24061.46 US PATENT & TRADELIMEN

Patent No.:

6,833,578

Serial No.:

OFFICE 10/733,216

Issue Date:

December 21, 2004

Entitled:

Method and Structure Improving

Isolation Between Memory Cell Passing Gate and Capacitor

Filing Date:

December 11, 2003

VIA FACSIMILE - 703-305-8568 Mail Stop PGPUB Commissioner for Patents PQ Box 1450 Alexandria, VA 22313-1450

## REQUEST FOR REFUND OF PRE-GRANT PUBLICATION FEE

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Patentoe hereby requests refluid of the pre-grant publication fee timely paid on November 10, 2004. A copy of Issue Per Transmittal, indicating payment of publication fee and return postcard is enclosed herowith.

According to the Updated Filing Receipt (copy enclosed) the pre-grant publication was scheduled to publish on June 16, 2005. Patentee notes that the subject application proceeded to allowance and issued prior to the projected publication date. It has been more than four weeks since the issue date and therefore, refund of the publication fee under 37 CFR 1.18(d) is respectfully requested.

Patentee respectfully requests that a refund of \$300.00 be credited to the Deposit Account of Haynes and Boone, LLP Number 08-1394. This form submitted in duplicate.

Respectfully submitted,

9-13-05

HAYNES AND BOONE, LLP 901 Main Street, Suito 3100 Dallas, Texas 75202-3789 Telephone: 972-739-8635 Facsimile: 214-200-0853

David M. O'Dell Registration No. 42,044

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2005 SEP 1.6 SH #1

Patentée:

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David M. O'Dell Attorney for Applicants Haynes and Boone, LLP 901 Main Street, Suite 3100 Dallas, TX 75202-3789

**CONFIRMATION NO. 8002** 

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Date Mailed: 04/19/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-8195. Please provide a copy of this Filing Receipt with the changes noted thereon, if you received a "Notice to File Missing Parts" for this application, please submit the training of the Filing Receipt with the changes noted the receipt of the Patent Filing Receipt with the changes noted the receipt of the Patent Filing Receipt with the changes noted the receipt of the Patent Filing Receipt with the patent for t any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filling Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kuo-Chi Tu, Hain-Chu, TAIWAN; Chun-Yao Chen, Hstn-Chu, TAIWAN; Huey-Chi Chu, Helnchu City, TAIWAN; Huey-Chi Chu, Hsinchu City, TAIWAN; Chung-Wei Chang, Hsin-Chu, TAIWAN; Tien-Lu Lin, Hsinchu City, TAIWAN; Kuo-Ching Huang, Kachsiung, TAIWAN; Wen-Cheng Chen, Hsin-Chu County, TAIWAN; Tsung-Haun Huang, Taipei County, TAIWAN; Hsiao-Hui Tseng, Tainan City, TAIWAN;

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Assignment For Published Patent Application

HAYNES & BOONE LLP. Taiwan Semiconductor Manufacturing Co., Ltd., Hsin-Chu, TAIWAN;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 03/17/2004

Projected Publication Date: 06/16/2005

Non-Publication Request: No

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Docketed 4.21-05

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#### Early Publication Request No



Title

Method and structure improving isolation between memory cell passing gate and capacitor

**Preliminary Class** 

365

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